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ScalA16: Workshop on Latest Advances in Scalable Algorithms for Large-Scale Systems 2016-11-13

Background

- ► Domain: atomic-level molecular dynamics
	- \triangleright Classical MD: good scalable methods, but static bonding and fixed charges limit modeling capability
	- \triangleright Reactive, polarizable MD: expand modeling ability but pose new computational challenges: dynamic charge distribution for reactivity
		- \blacktriangleright Idea: distribute charge (electrons) to atoms according to min. energy configuration (ground state)
- Goal: improve performance of expensive, poorly scaling charge distribution kernel
- \blacktriangleright Models for Charge Distribution:
	- \triangleright charge equilibration (QEq)
	- \blacktriangleright electronegativity equilibration (EEM)
	- ► atom condensed Kohn-Sham DFT approximated to second order (ACKS2)
	- \triangleright split charge equilibration (SQE)
	- \triangleright others (active research)
- \blacktriangleright Approaches for Finding Charges in Models:
	- \blacktriangleright linear solvers: iterative (GMRES with preconditioning)
	- \blacktriangleright extended Lagrangian

QEq Overview

Minimize
$$
\mathbf{E}_{\text{ele}}(\mathbf{Q}) = \sum_{i} \chi_i q_i + \frac{1}{2} \sum_{i,j} H_{ij} q_i q_j
$$

\nwhere $H_{ij} = J_i \delta_{ij} + \frac{1 - \delta_{ij}}{\sqrt[3]{r_{ij}^3 + \gamma_{ij}^{-3}}}$
\nsubject to $Q_{net} = \sum_{i} q_i$

After applying the Lagrange multiplier method:

$$
-\chi_k = \sum_i H_{ki} s_i, \qquad k = 1, ..., n
$$

$$
-1 = \sum_i H_{ki} t_i, \qquad k = 1, ..., n
$$

where:

$$
q_i = s_i - \frac{\sum_i s_i}{\sum_i t_i} \cdot t_i
$$

- $\mathbf{R} = (r_1, r_2, \dots, r_n)$: positions in system with *n* atoms ($\in \mathbb{R}^3$)
- $\blacktriangleright \mathbf{Q} = (q_1, q_2, \ldots, q_n)$: partial charges (unknowns)
- $\blacktriangleright \chi_i$: electronegativity of atom i
- \blacktriangleright J_i : idempotential of atom i
- \blacktriangleright $r_{ij} = ||r_j r_i||_2$: distance between atoms i and j
- \triangleright γ : electrostatic shielding term

Preconditioner Design and Considerations

Design

- \triangleright Idea: use good initial guesses (already done via extrapolation)
- \triangleright Idea: apply preconditioning to GMRES residual vector
	- ► Relatively different than those for PDE discretizations: much higher NNZ per row on average

Considerations

- \triangleright Effectiveness: reduction in num. of solver iters.
- \triangleright Cost: computation and application time
- \blacktriangleright Longevity: num. of sim. iters. for which preconditioner is usable
- \triangleright Parallelizabilty: shared mem. scalability of preconditioner
	- \blacktriangleright Important building block for dist. mem. algorithms!

Preconditioner Computation Methods

Basic Techniques

 \triangleright Diagonal inverse (Diag)

$$
\mathbf{P}_{ij}^{-1} = \begin{cases} \frac{1}{\mathbf{H}_{ij}}, & i = j \\ 0, & \text{otherwise} \end{cases}
$$

- \triangleright Incomplete factorizations with 0 fill-in
	- \triangleright Cholesky with level scheduling $(IC(0))$
		- \blacktriangleright Idea: analyze sparsity pattern, perform parallel operations on independent levels

$$
\mathbf{H} \approx \mathbf{U}^{\mathbf{T}} \mathbf{U}
$$

- Fine grained $(FG-ILU(0))$
	- \blacktriangleright Idea: asynchronous constraint-based approach

 $H \approx LU$

Preconditioner Computation Methods

Enhanced Techniques

- \triangleright Distance drop (IC-dist(d), FG-ILU-dist(d))
	- \blacktriangleright Insight: off-diagonal entries are inversely proportional to distance between atoms

 $r_{ii} > r_{\text{nonb}} \cdot d, \qquad d \in (0, 1)$

- \triangleright Dual drop (**IC-dual(t)**, **FG-ILU-dual(t)**)
	- \blacktriangleright Numeric thresholding based on row-wise 1-norm

$$
H_{ij} > ||H_{i,:}||_1 \cdot t, \qquad t \in \mathbf{R}^+
$$

 \blacktriangleright Apply during and post factor computation for **IC-dual(t)**, but only post for FG-ILU-dual(t) to achieve convergence

Preconditioner Application Methods

Techniques for Incomplete Factorizations

- \blacktriangleright Level scheduling
	- \triangleright Symbolic stage: find available parallelism in triangular solves (once per preconditioner computation)
	- \triangleright Numeric stage: perform solves in parallel per level
	- ► Combine with $IC(0)$, $IC-*(*)$ computation methods
- \blacktriangleright Jacobi iteration
	- \blacktriangleright Approximate traingular solve:
		- \blacktriangleright To solve the triangular system $Fx = b$, instead iterate

$$
x_{k+1} = \left(I - D^{-1}F\right)x_k + D^{-1}b
$$

- ▶ Fixed num. of iters. (SpMV, vector-vector ops.)
- \triangleright Combine with **FG-ILU(0)**, **FG-ILU-** $*(*)$ computation methods

Numeric Experiments

Setup

- \blacktriangleright Platform
	- ► NERSC Edison: Cray XC30
		- ▶ 12-core Intel Ivy Bridge, 2.4 GHz (dual socket)
		- ▶ Private L1 (32 KB inst., 32 KB data), L2 (256 KB) caches
		- \blacktriangleright Shared 30 MB L3
		- \triangleright 64 GB DDR3, 1866 MHz bus
- \triangleright Software and Tools
	- \blacktriangleright PuReMD ReaxFF
	- \blacktriangleright Intel compiler v15.0.1
	- \triangleright Optimization flags: -fast

 \triangleright Chemical Systems

Numeric Experiments

Cost-Effectiveness Trade-off

Figure: Effectiveness vs. expected cost of various preconditioned GMRES solvers (averaged across all four benchmark systems)

Numeric Experiments

Longevity

Figure: FG-ILU-dual(0.01) for the bulk water system, 100 steps before re-computing preconditioner

Numeric Experiments

Overall Solver Performance

Figure: Average QEq times for different solvers, numbers over each bar group show the speedup achieved by the IC-dual scheme over the Diag solver

Numeric Experiments

Thread Scalability

Figure: IC-dual(0.01) with water (left) and PETN (right) at solver tol. 10^{-14}

Table: Num. of levels in preconditioner factors

Future Work and Acknowledgements

Future Work

- \triangleright Reduce num. levels and sync. of PreApp through explicit permutations via graph coloring
- \triangleright Experiment with further combinations of enhanced preconditioning techniques
- \blacktriangleright Implement approaches on many-core architectures (Xeon Phi)
- \triangleright Develop an automated parameter tuning method

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